# Steiner Transitive-Closure Spanners of Low-Dimensional Posets

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### **Graph Spanners**

#### **k-spanner (stretch k)**: Graph $G(V,E) \rightarrow$ H(V, $E_H \subseteq E$ ), if for all pairs of vertices (u, v) in G: $distance_H(u,v) \leq k \cdot distance_G(u,v)$



[Awerbuch '85, Peleg-Schäffer '89]

## **Transitive-Closure Spanners**

# **Transitive closure** TC(*G*) has an edge from *u* to *v* iff *G* has a path from *u* to *v*



H is a k-TC-spanner of G if H is a subgraph of TC(G)TC(G) f dr-TC-spanner of G if H is a subgraph of TC(G) f dr-TC-spanner of G if H is if k-spanner of H is if h-spanner of H is a subgraph of H is if h-spanner of H is a subgraph of

2-TC-spanner of G

Bhattacharyya, Grigorescu, Jung, Raskhodnikova, Woodruff, SODA'09], generalizing [Yao 82; Chazelle 87; Alon, Schieber 87, ...]

Shortcut edge

consistent with

# **Applications of TC-Spanners**

- Data structures for storing partial products [Yao, '82; Chazelle '87, Alon, Schieber, 88]
- Constructions of unbounded fan-in circuits [Chandra, Fortune, Lipton ICALP, STOC'83]
- Property testers for monotonicity and Lipschitzness [Dodis et al. '99,BGJRW'09; Jha, Raskhodnikova, FOCS '11]
- Lower bounds for reconstructors for monotonicity and Lipshitzness [BGJJRW'10, JR'11]

Efficient key management in access hierarchies

Follow references in [Raskhodnikova '10 (survey)]

# **Application to Access Control**



To speed up key derivation time, add shortcut edges consistent with permission edges

[Attalah, Frikken, Blanton '05; Attalah, Blanton, Frikken '06, De Santis, Ferrara, Massuci '07, Attalah, Blanton, Fazio, Frikken '09]



*H* is a **Steiner** *k***-TC-spanner** of *G* if

- $vertices(G) \subseteq vertices(H)$
- $distance_H(u,v) \le k$  if *G* has a path from *u* to *v*

∞ otherwise

#### **Bounds for Steiner TC-Spanners**

• For some graphs Steiner TC-spanners are still large:



<u>FACT</u>: For a random directed bipartite graph of density  $\frac{1}{2}$ , any Steiner 2-TCspanner requires  $\widehat{\Omega}(n^2)$ edges.

## **Low-Dimensional Posets**

- [ABFF 09] access hierarchies are low-dimensional posets
- Poset  $\equiv$  DAG (edge  $(u, v) \equiv u \leq v$ )
- Poset G has dimension d if G can be <u>embedded</u> into a <u>hypergrid</u> of dimension d and d is minimum.



 $e: G \to G'$  is a **poset embedding** if for all  $x, y \in G$ ,  $x \leq_G y$  iff  $e(x) \leq_{G'} e(y)$ .

**Hypergrid**  $[m]^d$  has ordering  $(x_1, ..., x_d) \leq (y_1, ..., y_d)$  iff  $x_i \leq y_i$  for all *i* 

# **Our Main Results**



#### **Facts about Poset Dimension**

- Hypergrid [m]<sup>d</sup> has dimension exactly *d*.
   [Dushnik-Miller 40]
- Finding a poset dimension is NP-hard.
   [Yannakakis 82]

 Assumption (as in [ABFF 09]): poset embedding into a hypergrid of minimal dimension is explicitly given.

#### **Lower Bound for** k = 2

- We prove lower bound for size of Steiner 2-TC-spanner for the hypergrid [m]<sup>d</sup>.
   Steiner points can be embedded
  - Minimal size of Steiner 2-TC-spanner = Minimal size of (non-Steiner) 2-TC-spanner
  - Lower bound for size of 2-TC-spanner

## **2-TC-Spanner for** $[m]^d$



#### **Lower Bound Strategy**

- Write in IP for a minimal 2-TC-spanner.
  OPT ≥ LP = LP<sub>dual</sub>
- Integrality gap of the primal is small.
- Idea: Construct some feasible solution for the dual => lower bound on OPT.
- OPT  $\geq LP = LP_{dual} \geq LB$

#### **IP Formulation**

• {0,1}-program for Minimal 2-TC-spanner:

minimize



subject to:



 $\forall u \leq w \leq v \\ \forall u \leq w \leq v$ 

 $\forall u \leq v$ 

 $\sum p_{uwv} \ge 1$  $w: u \leq w \leq v$ 

#### **Dual LP**

maximize

• Take fractional relaxation of IP and look at its dual:

$$\sum_{u,v:u\leqslant v} y_{uv}$$

subject to: 
$$\sum_{w:v \leq w} q_{uvw} + \sum_{w:w \leq u} r_{wuv} \leq 1 \quad \forall u \leq v$$

$$\begin{array}{ll} y_{uv} \leq q_{uwv} + r_{uwv} & \forall u \leq w \leq v \\ 0 \leq y_{uv}, q_{uwv}, r_{uwv} \leq 1 & \forall u \leq w \leq v \end{array}$$

#### **Constructing solution to dual LP**

• Now we use fact that poset is a hypergrid! For  $u \leq v$ , set  $y_{uv} = \frac{1}{V(v-u)'}$ , where V(v - u) is volume of box with corners u and v.



#### **Constructing solution to dual LP**

• Now we use fact that poset is a hypergrid! For  $u \leq v$ , set  $y_{uv} = \frac{1}{(4\pi)^d V(v-u)'}$ , where V(v-u) is volume of box with corners u and v.



## Wrap-up

- Upper bound for Steiner 2-TC-spanner
- Lower bound for 2-TC-spanner for a hypergrid.
  - Previous bound: combinatorial, ~10 pages, not tight.
  - Technique: find a feasible solution for the dual LP
- Lower bound of  $\Omega(n \log^{\lceil (d-1)/k \rceil} n)$  for  $k \ge 3$ .
  - Combinatorial
  - Holds for randomly generated posets, not explicit.
- OPEN PROBLEM:
  - Can the LP technique give a better lower bound for k ≥ 3?

